

CLAIMS

1. In a Java computing environment, a Java macro instruction representing:
a sequence of Java Bytecode instructions in a Java programming
5 loop;
wherein said Java macro instruction can be executed by a Java
virtual machine operating in said Java computing environment, and
wherein, when said Java macro instruction is executed, the
operations that are performed by said conventional sequence of Java
10 Bytecode instructions are performed.
2. A Java macro instruction as recited in claim 1, wherein said sequence is
a conventional Java Bytecode sequence which includes a conventional
conditional flow control instruction.
- 15 3. A Java macro instruction as recited in claim 1, wherein said Java macro
instruction is generated during the Java Bytecode verification phase.
4. A Java macro instruction as recited in claim 1, wherein said Java virtual
20 machine internally represents Java instructions as a pair of streams.
5. A Java macro instruction as recited in claim 4,
wherein said pair of streams includes a code stream and a data
stream,
25 wherein said code stream is suitable for containing a code portion of
said Java macro instruction, and
wherein said data stream is suitable for containing a data portion of
said Java macro instruction.

6. A Java macro instruction as recited in claim 5,

wherein said Java macro instruction is generated only when said virtual machine determines that said Java macro instruction should replace said sequence.

7. A Java macro instruction as recited in claim 6, wherein said determination is made based on a predetermined criteria.

8. A Java macro instruction as recited in claim 7, wherein said predetermined criteria is whether said sequence has been repeated more than a predetermined number of times.

9. In a Java computing environment, a Java macro instruction representing:
a sequence of Java Bytecode instructions in a Java programming loop,

wherein said sequence of Java Bytecode instructions are in a reduced set of virtual machine instructions suitable for execution in a virtual machine, the reduced set of virtual machine instructions representing a number of corresponding Java Bytecode executable instructions that are also suitable for execution in the virtual machine,

wherein the set of the virtual machine instructions consists of a number of virtual machine instructions which is less than the number of the corresponding Java Bytecode executable instructions, and

wherein every one of the corresponding Java Bytecode executable instructions can be represented by at least one of the virtual machine instructions in the virtual machine instruction set.

10. A Java macro instruction as recited in claim 9, wherein said Java macro instruction is generated during the Java Bytecode verification phase.

11. A Java macro instruction as recited in claim 9,

wherein said Java macro instruction is generated only when said virtual machine determines that said Java macro instruction should replace said sequence.

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12. A Java macro instruction as recited in claim 11, wherein said determination is made based on a predetermined criteria.

13. A Java macro instruction as recited in claim 12, wherein said predetermined criteria is whether said sequence has been repeated more than a predetermined number of times.

14. A Java macro instruction as recited in claim 9, wherein the number of virtual machine instructions is about 30 to 50 percent of the number of the corresponding Java Bytecode executable instructions.

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15. A computer readable media including computer program code for a Java macro instruction, said Java macro instruction representing:

a sequence of Java Bytecode instructions in a Java programming loop,

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wherein said sequence of Java Bytecode instructions are in a reduced set of virtual machine instructions suitable for execution in a virtual machine, the reduced set of virtual machine instructions representing a number of corresponding Java Bytecode executable instructions that are also suitable for execution in the virtual machine,

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wherein the set of the virtual machine instructions consists of a number of virtual machine instructions which is less than the number of the corresponding Java Bytecode executable instructions, and

wherein every one of the corresponding Java Bytecode executable instructions can be represented by at least one of the virtual machine instructions in the virtual machine instruction set.

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16. A computer readable media as recited in claim 15, wherein said Java macro instruction is generated during the Java Bytecode verification phase.

17. A computer readable media as recited in claim 16, wherein said Java macro instruction is generated only when said virtual machine determines that said Java macro instruction should replace said sequence.

18. A computer readable media as recited in claim 17, wherein said determination is made based on a predetermined criteria.

19. A computer readable media as recited in claim 18, wherein said predetermined criteria is whether said sequence has been repeated more than a predetermined number of times.

20. A computer readable media as recited in claim 19, wherein the number of virtual machine instructions is about 30 to 50 percent of the number of the corresponding Java Bytecode executable instructions.